

FIGURE 1

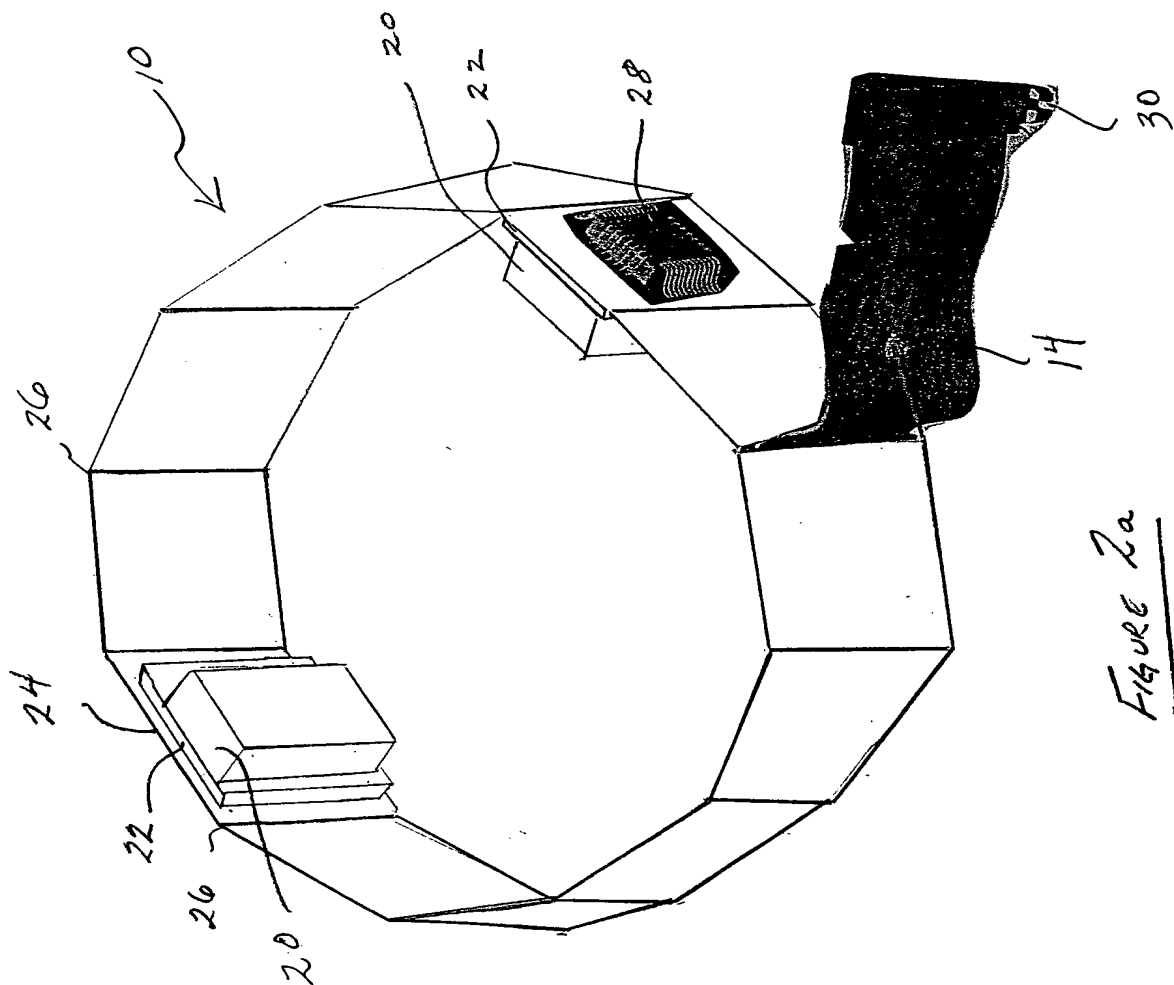


FIGURE 2a

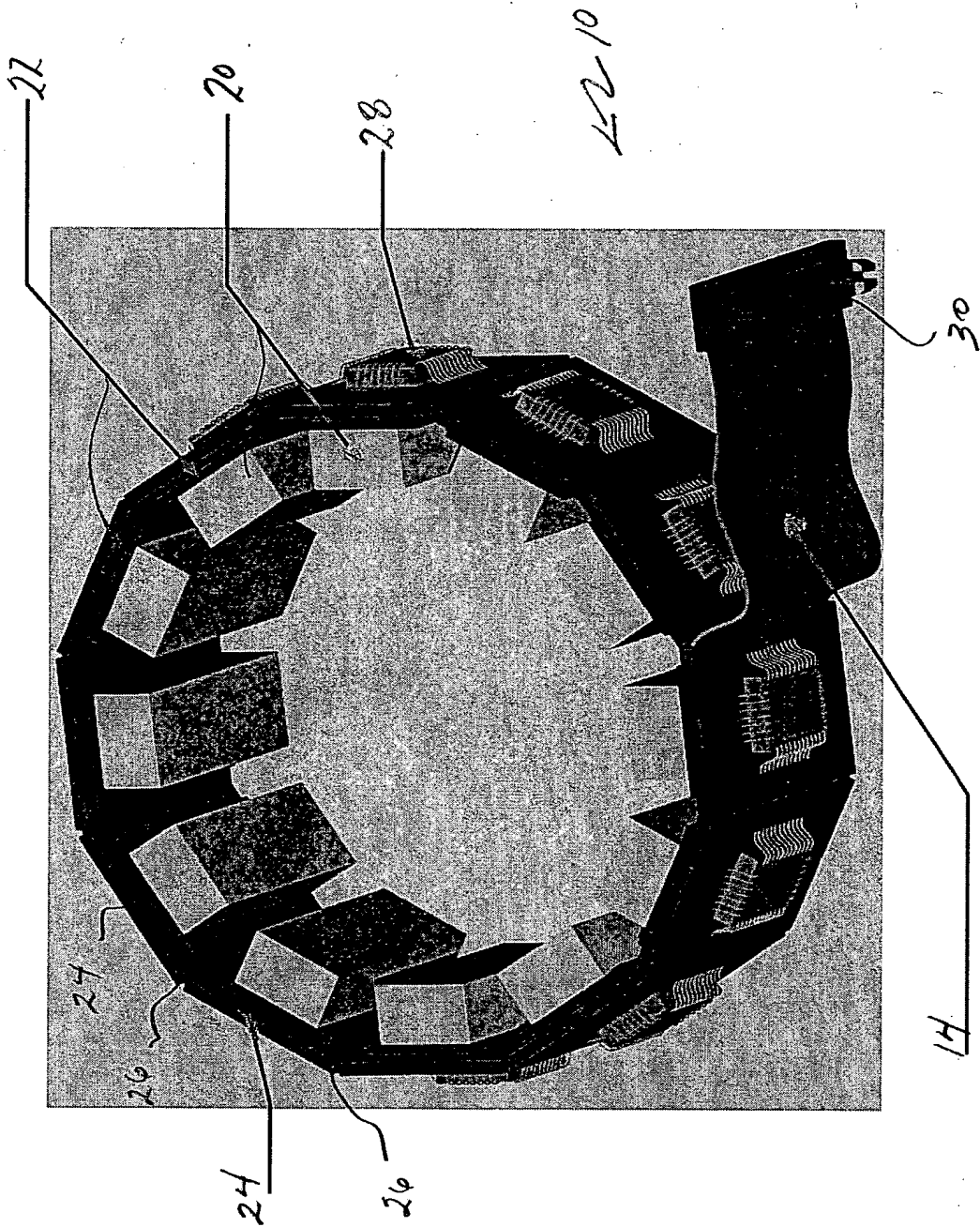


FIGURE 2b

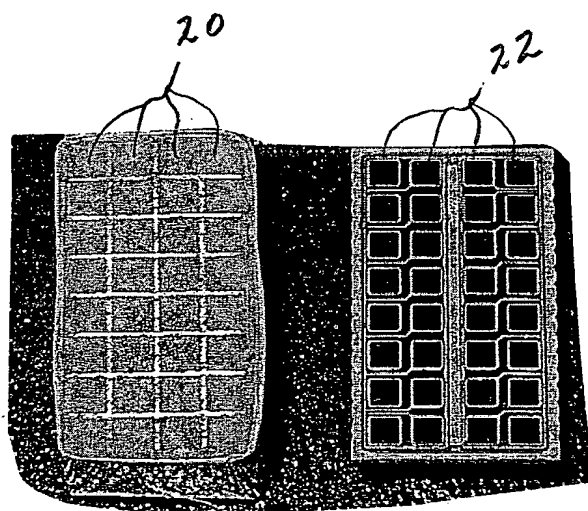


FIGURE 3

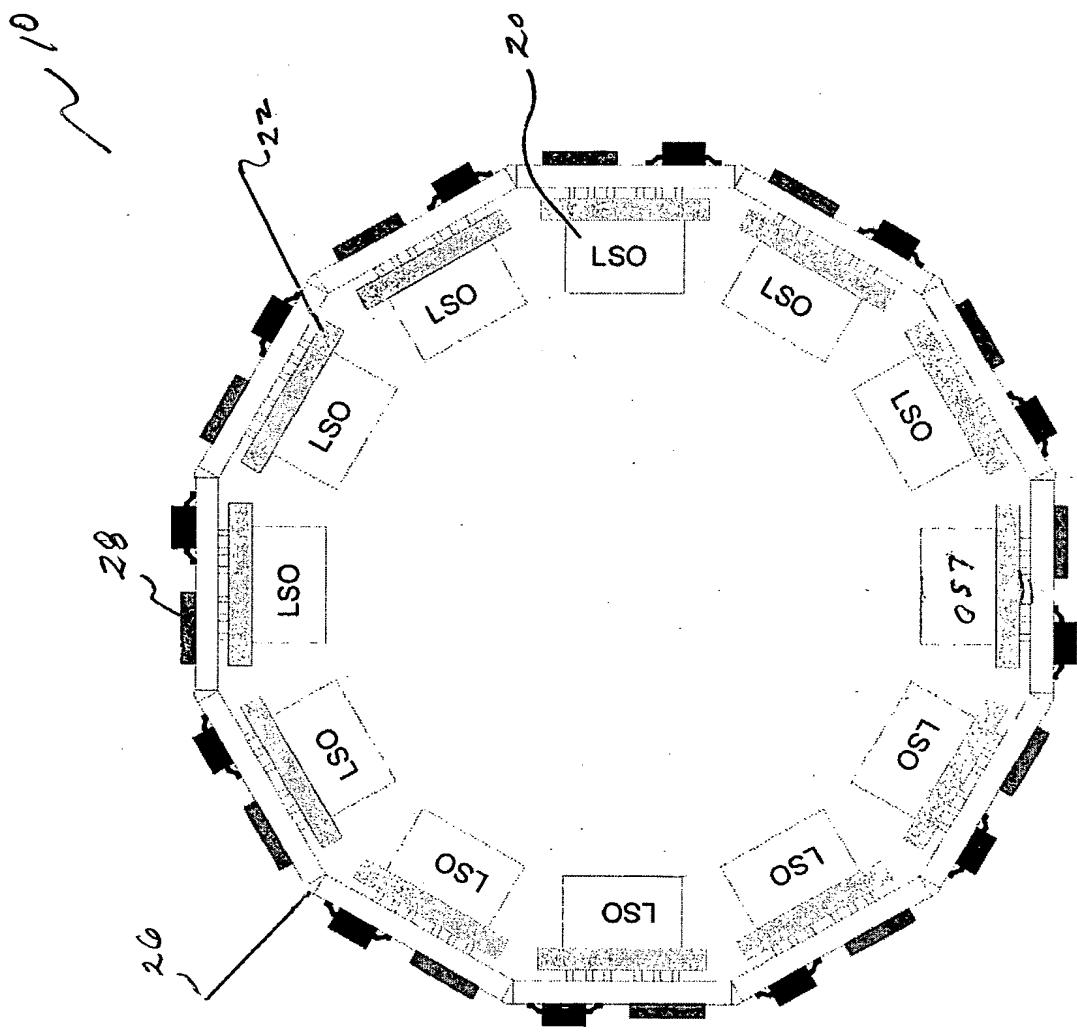


FIGURE 4

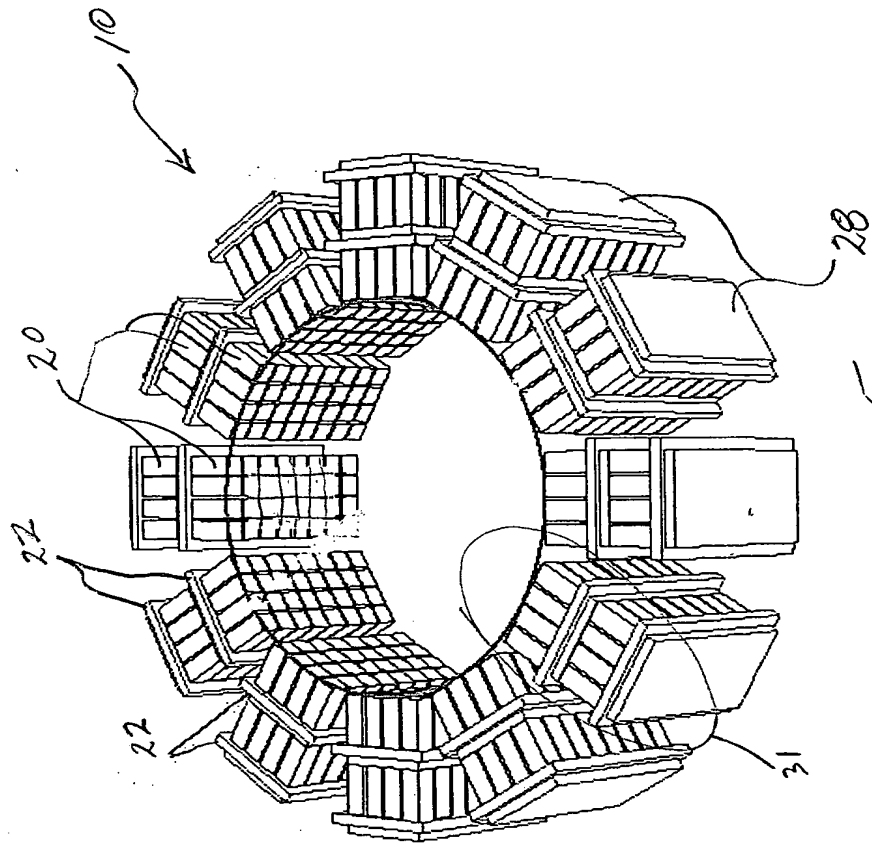


FIGURE 5

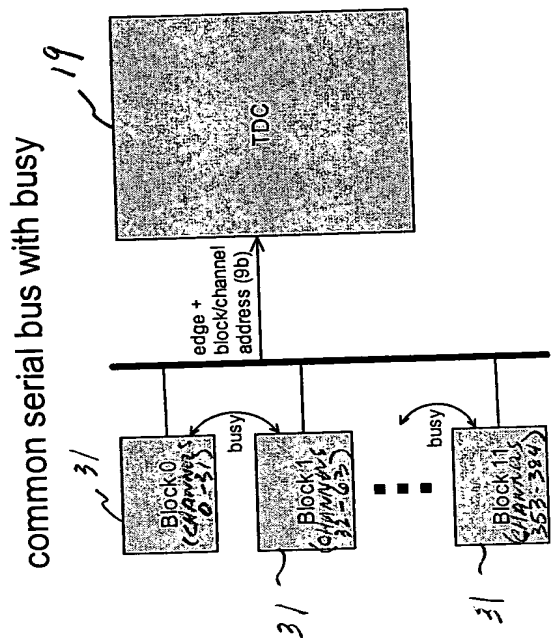


FIGURE 7

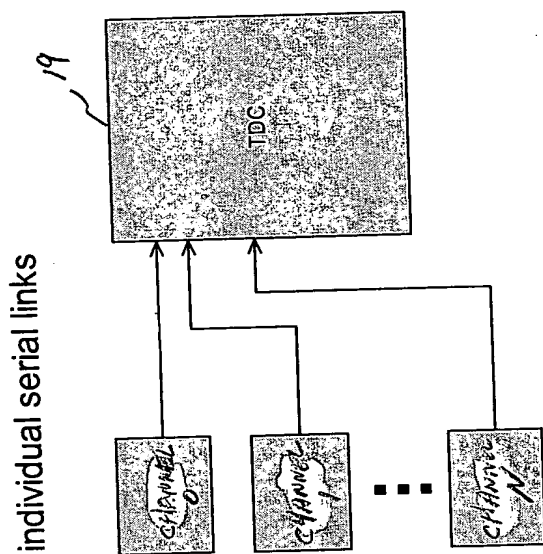


FIGURE 6
PRIOR ART

Serial link timing

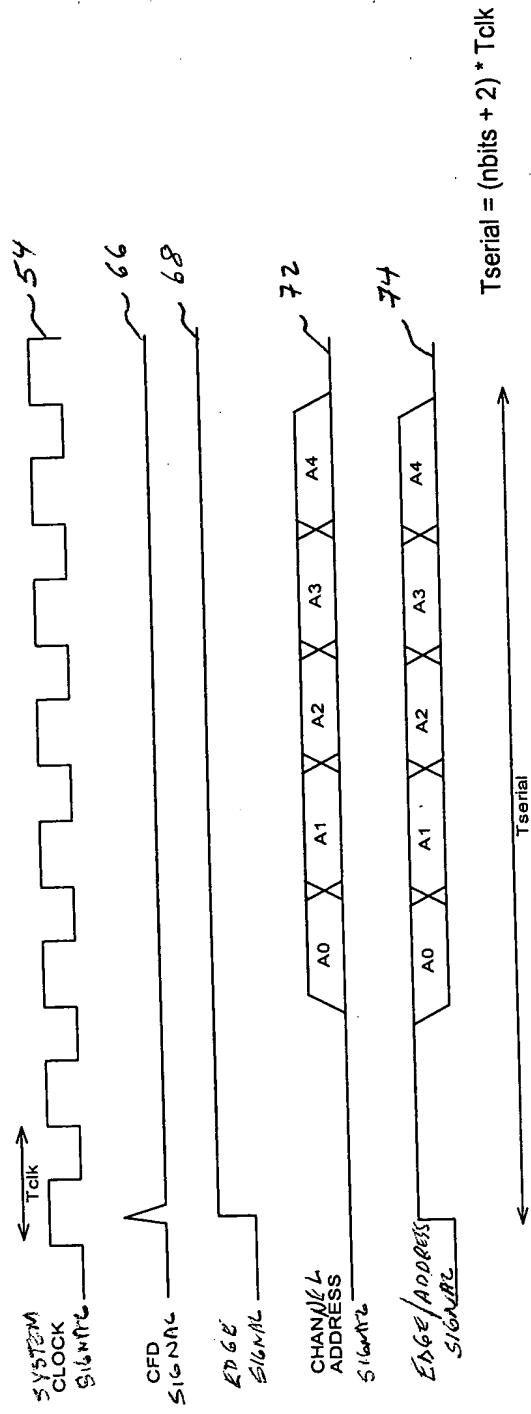


FIGURE 8

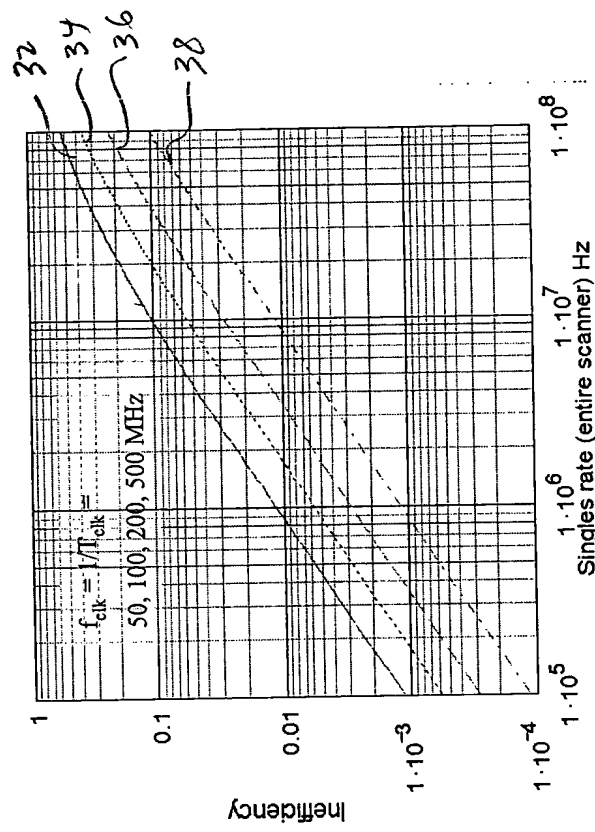


FIGURE 9

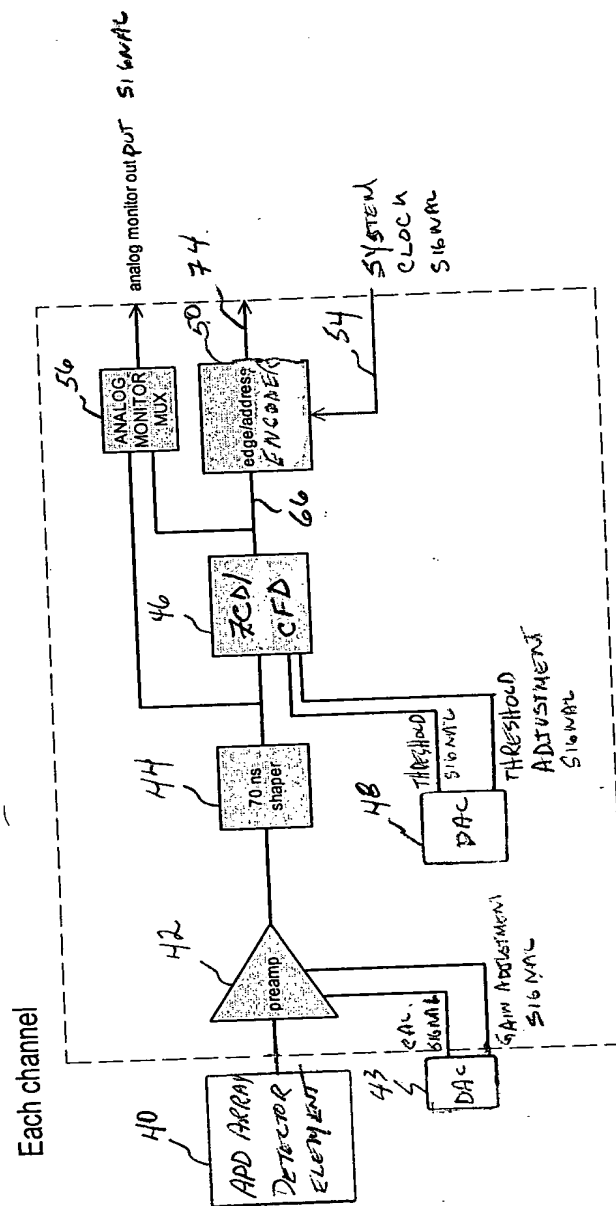


FIGURE 10

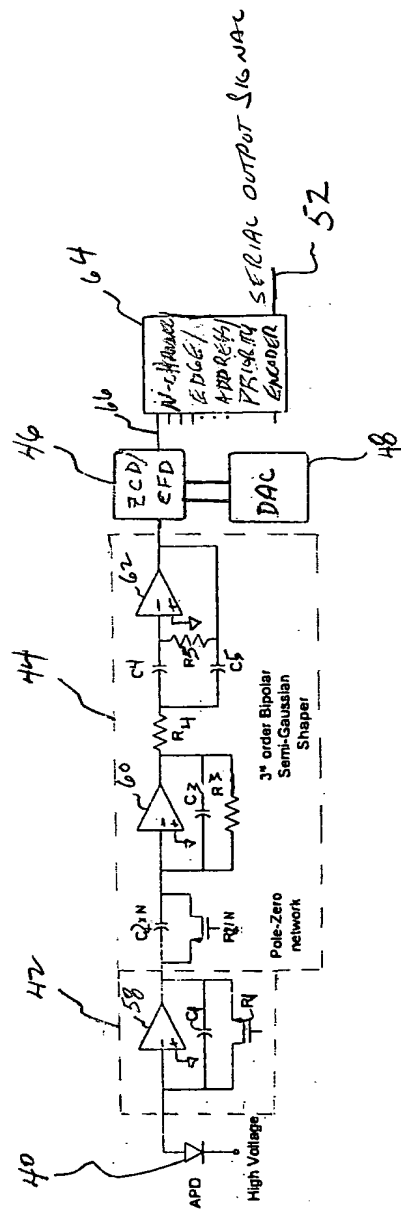


FIGURE 11

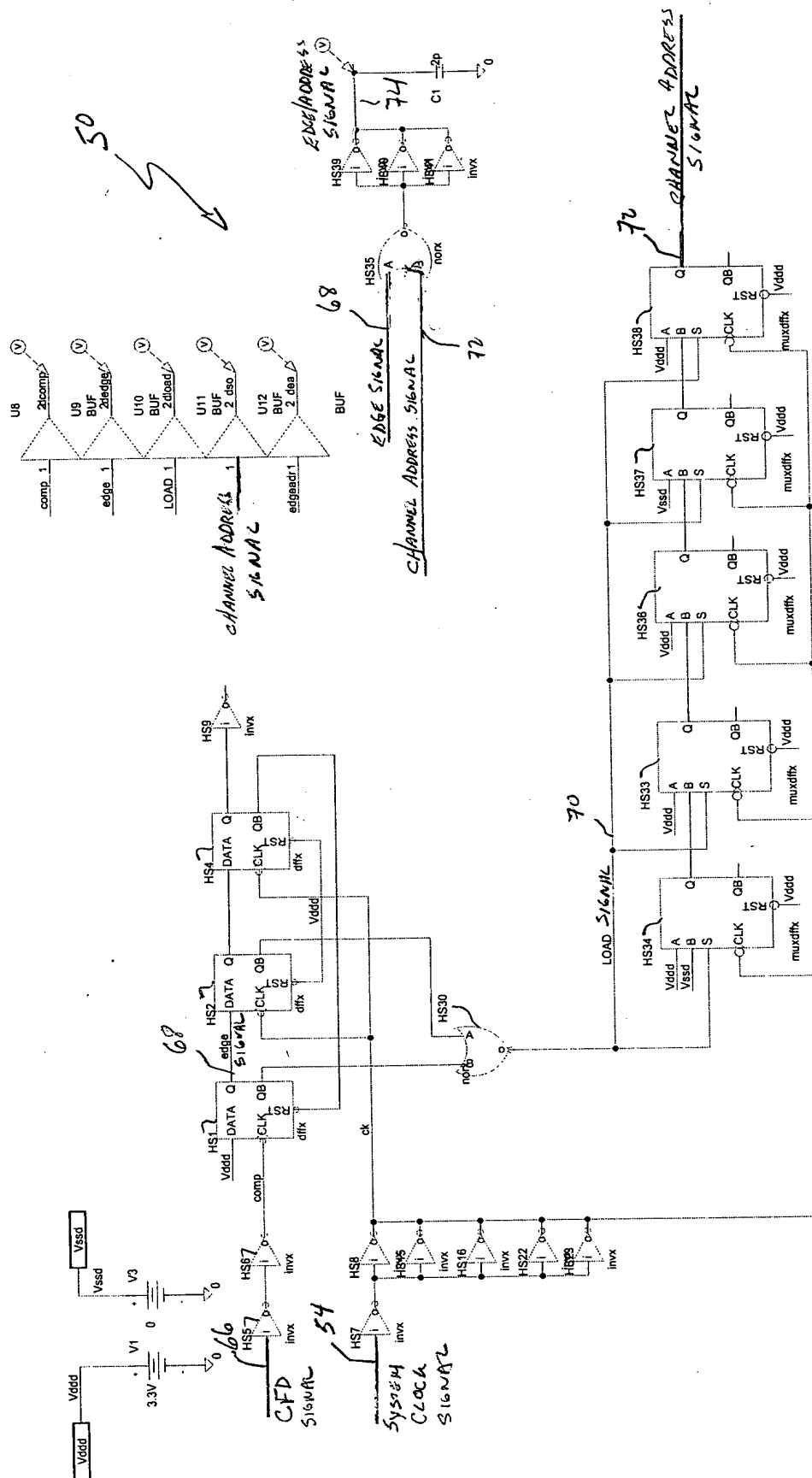
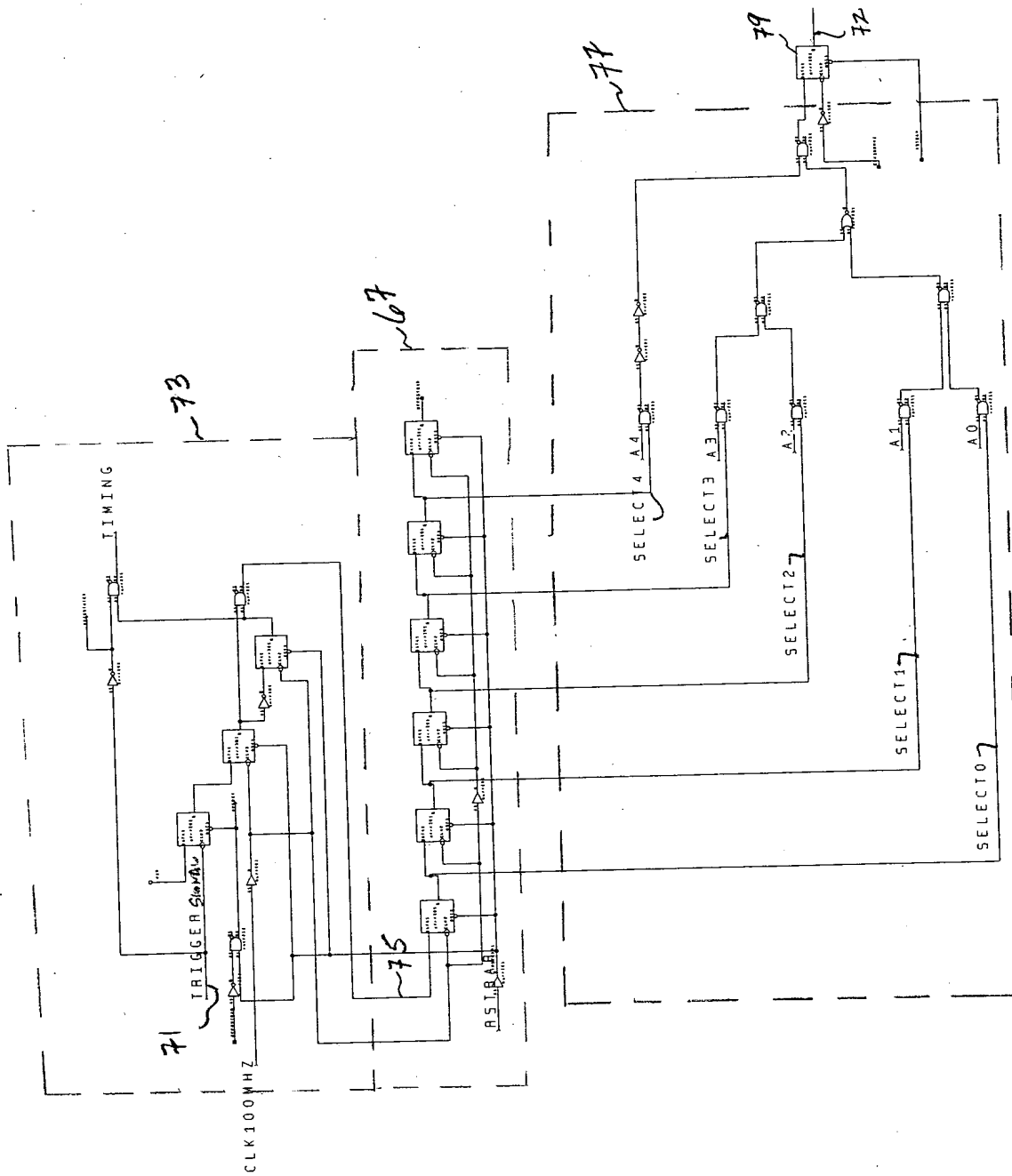


Figure 12



TRIGGER=Asynchronous Trigger.

A[4:0]=5 Bit Crystal ID from the 32 to 5 Priority Encoder

SERADD=serialized 5 bit Crystal ID at 100MHz

FIGURE 12-2

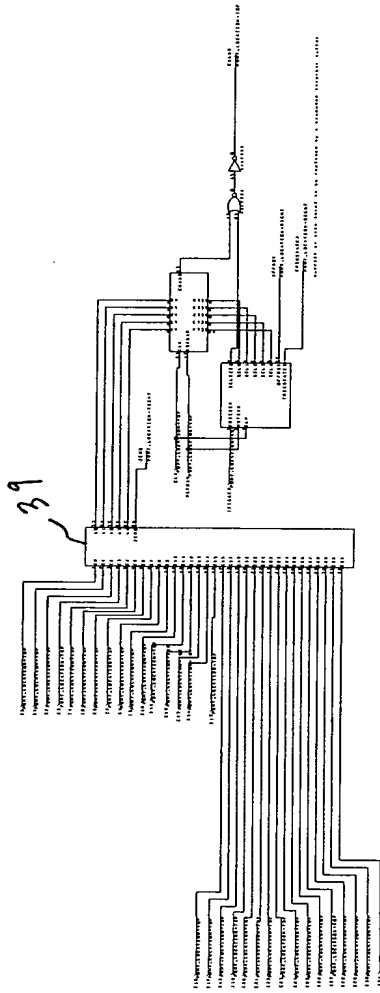


Figure 13

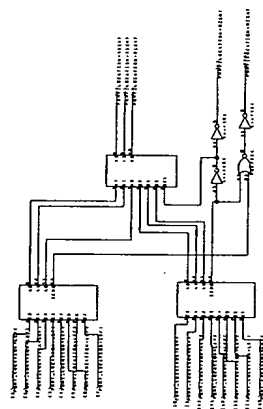


FIGURE 14

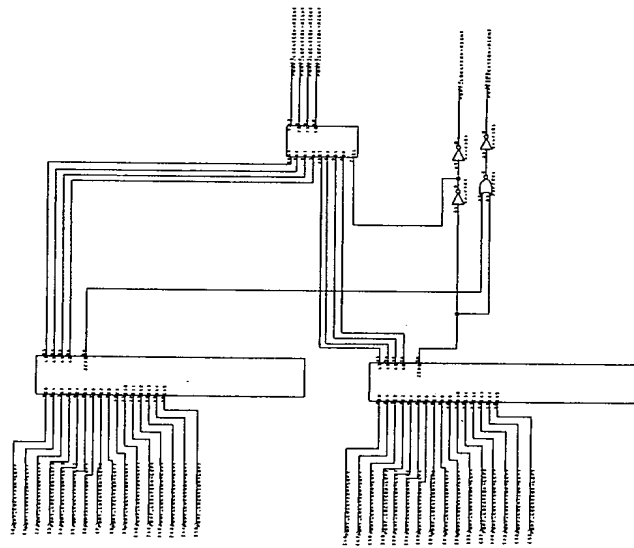


FIGURE 15

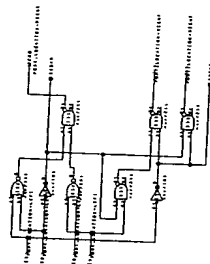


FIGURE 16

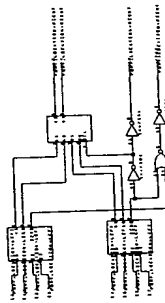


FIGURE 17

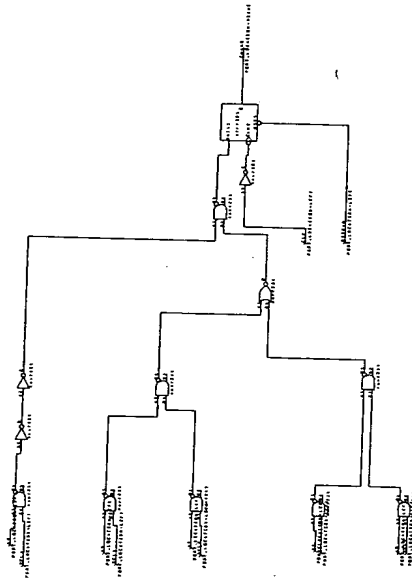


FIGURE 18

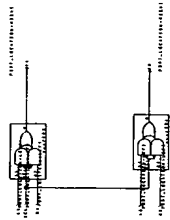


Figure 19

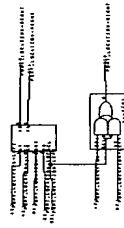


Figure 20

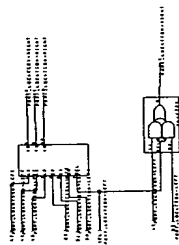


FIGURE 21

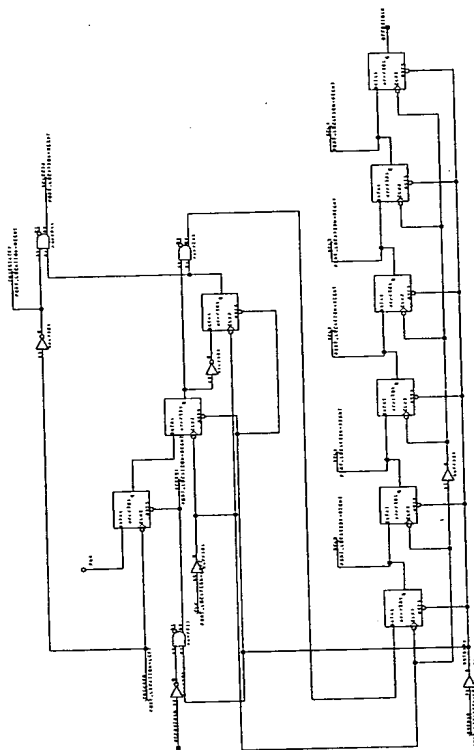


FIGURE 22

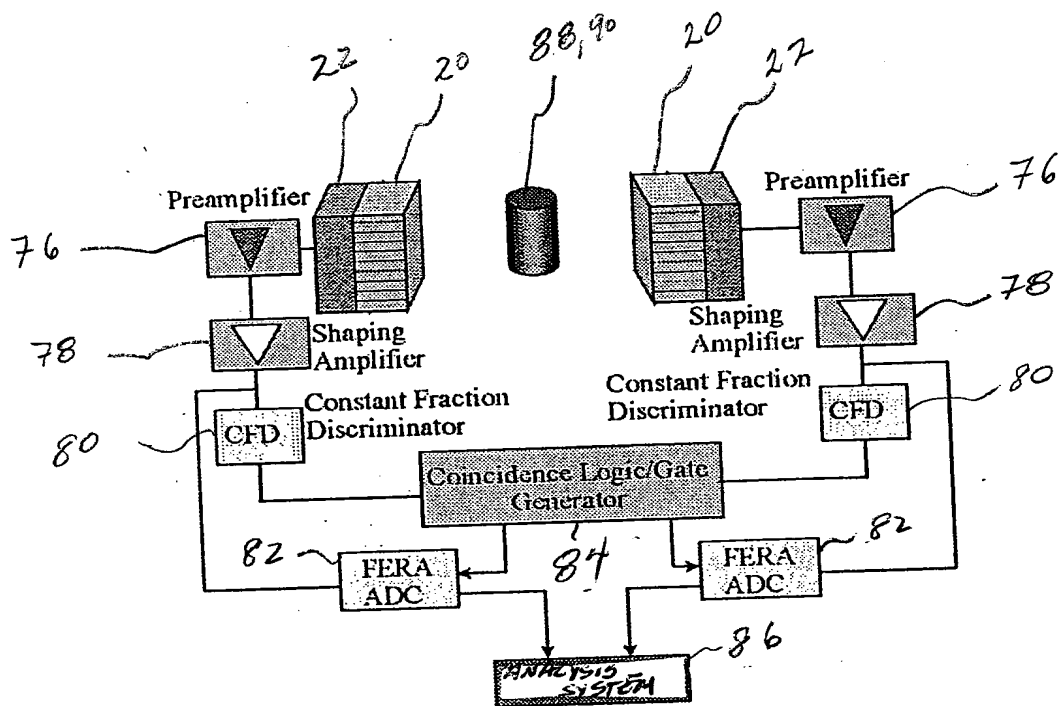


FIGURE 23

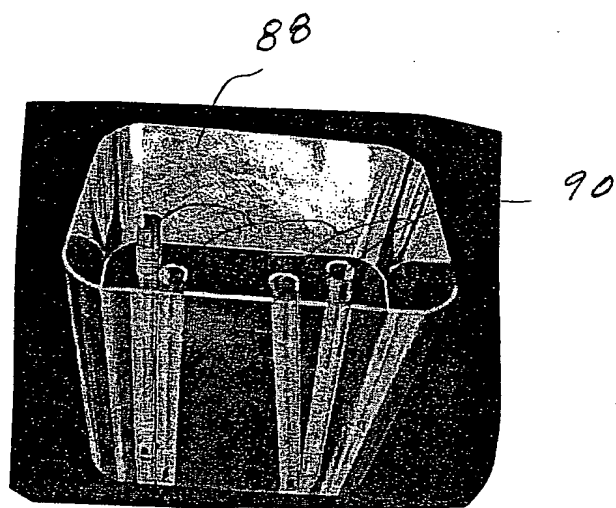


FIGURE 24

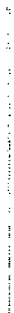


FIGURE 24a

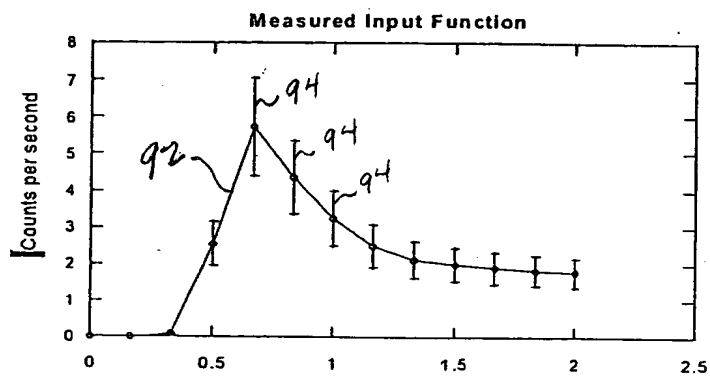


FIGURE 25

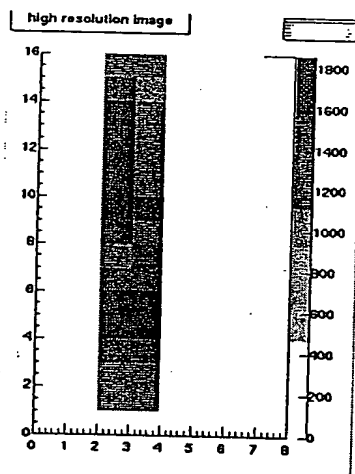


FIGURE 26

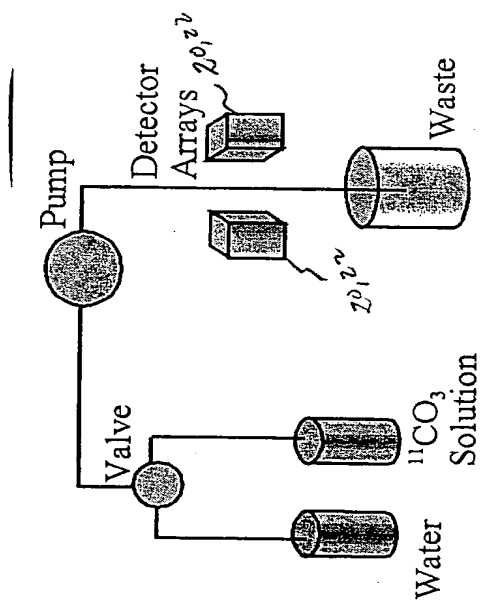


FIGURE 26a

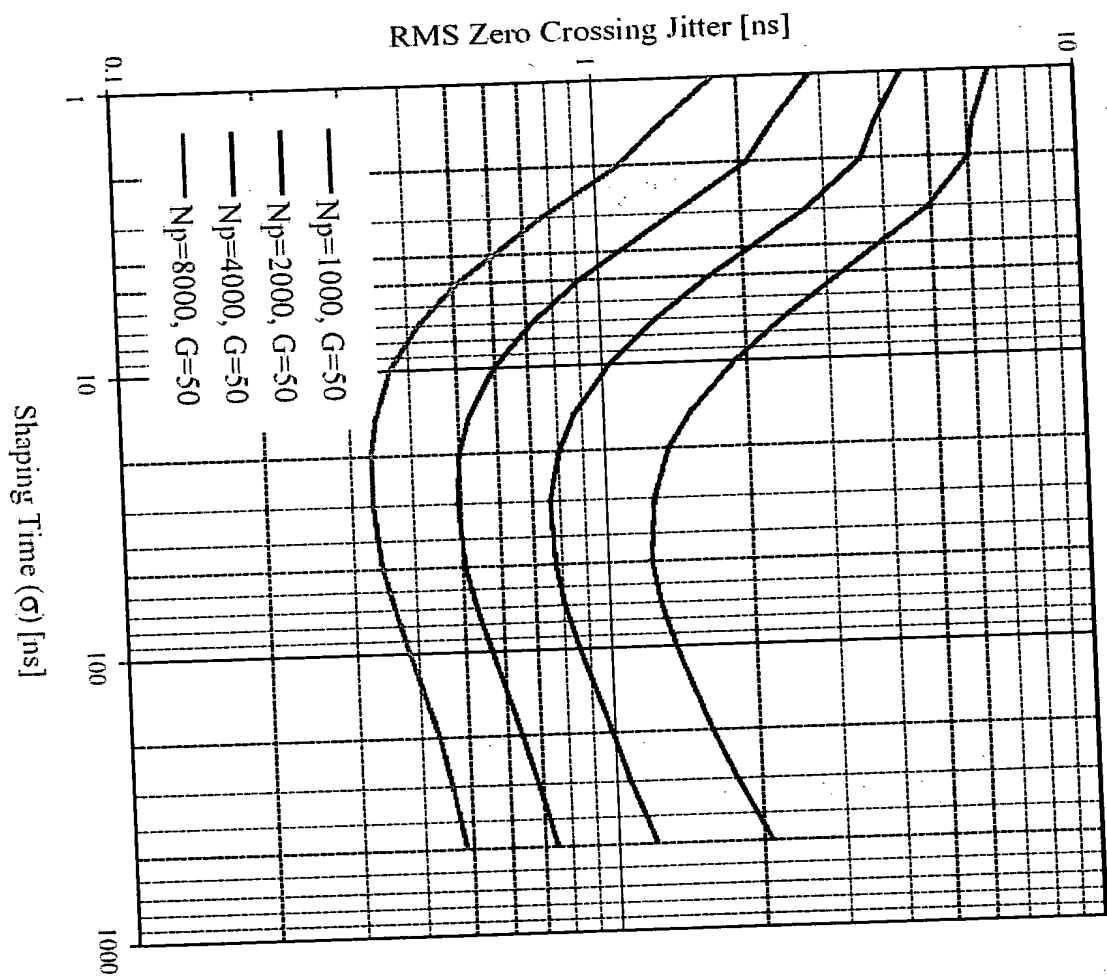


FIGURE 27

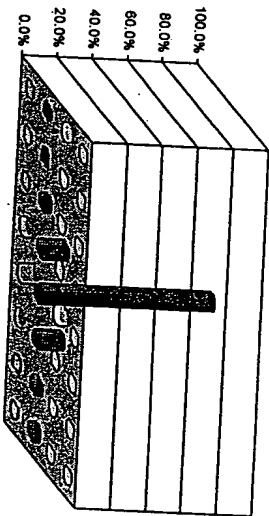


FIGURE 28